UTILITY **PATENT APPLICATION TRANSMITTAL**

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Attorney Docket No. 241737US2

First Inventor or Application Identifier Atsushi MASUDA

Title DESIGN METHOD OF LOGIC CIRCUIT

Assignee Name:

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APPLICATION ELEMENTS See MPEP chapter 600 concerning utility patent application contents			Commissioner for Patents ADDRESS TO: Mail Stop Patent Application Alexandria, Virginia 22313							
1.		Fee Transmittal Form (e.g. PTO/SB/17) (Submit an original and a duplicate for fee processing)	ACCOMPANYING APPLICATION PARTS							
			7. 🗆 Assig	nment Papers (cover s	heet & document(s))					
2.		Specification Total Sheets 50	8. 📕 Applic	cation Data Sheet. See	e 37 CFR 1.76					
			9. 🗆 37 C.I	F.R. §3.73(b) Statementhere is an assignee)	nt Power of Attorney					
3.		Drawing(s) (35 U.S.C. 113) Total Sheets 10	10. 🗆 Englis	sh Translation Docume						
				mation Disclosure ment (IDS)/PTO-1449	Copies of IDS Citations (3)					
4.		Oath or Declaration Total Pages 2		ninary Amendment						
	a.	Newly executed (original)	13. White	Advance Serial No. P	ostcard					
	b.	Copy from a prior application (37 C.F.R. §1.63(d)) (for continuation/divisional with box 17 completed)		ied Copy of Priority Do	cument(s) (1)					
		i. DELETION OF INVENTOR(S) Signed statement attached deleting inventor(s) named in the prior application, see 37 C.F.R. §1.63(d)(2) and		cant claims small entity CFR 1.27	status.					
5.		1.33(b). CD-ROM or CD-R in duplicate, large table or Computer Program (Appendix)	16. 🔳 Other	Request for Prio	rity					
6.		Nucleotide and/or Amino Acid Sequence Submission (if applicable, all necessary)								
	a.	☐ Computer Readable Form (CRF)								
	b.	Specification or Sequence Listing on :								
	-	i. CD-ROM or CD-R (2 copies); or								
		ii. 🗌 Paper								
	C.	☐ Statements verifying identity of above copies								
17.	17. If a CONTINUING APPLICATION, check appropriate box, and supply the requisite information below:									
☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application no.:										
F	² rior	application information: Examiner:		Group Art						
For C	ОПТІ	iNUATION OR DIVISIONAL APPS only: The entire disclosure of the prior a	application, from wh	hich an oath or declaration is	supplied under Box 4b, is					
For CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 4b, is considered a part of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.										
		nd the specification by inserting before the first line the ser	ntence:							
☐ This application is a ☐ Continuation ☐ Division ☐ Continuation-in-part (CIP)										
of application Serial No. Filed on										
	This	application claims priority of provisional application Serial	·		ed					
19. CORRESPONDENCE ADDRESS										
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<u> </u>	gnati	(: Irvin MoClalland	an	Date:	8/19/03					
	Na	me: O. II VIII IVICCIEIIand		Registration No.:						

Registration Number 21, 124



241737US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S) Atsushi MASUDA

SERIAL NO:

New Application

FILING DATE: Herewith

FOR:

DESIGN METHOD OF LOGIC CIRCUIT

FEE TRANSMITTAL

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

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Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

Date:

8/19/03

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> C. Irvin McClelland Registration Number 21,124

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